



2

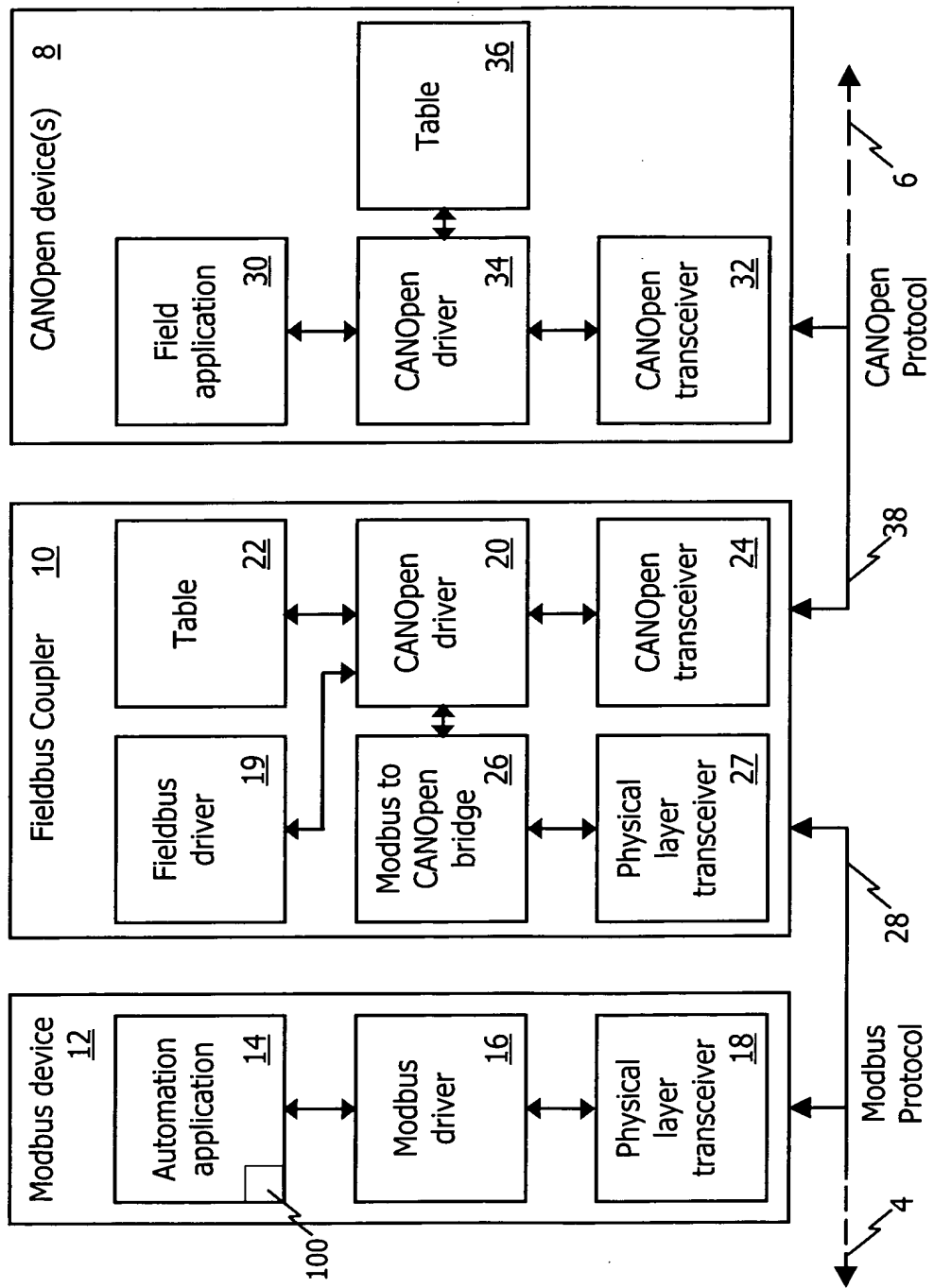


Fig. 1



42 ↘

Data	<u>46</u>
Function Code	<u>40</u>
Device ID	<u>44</u>

Fig. 2A

40 ↘

Error Check	<u>74</u>
Read/Write Data	<u>70</u>
Number Low Bytes	<u>68</u>
Number High Bytes	<u>66</u>
Starting Low Address	<u>64</u>
Starting High Address	<u>62</u>
Sub-Index	<u>60</u>
Index Low	<u>58</u>
Index High	<u>56</u>
Node ID	<u>54</u>
Extend Bit	<u>52</u>
Reference Type	<u>50</u>
Function Code "43"	
Slave ID	<u>72</u>

Fig. 2B



140 ↘

Error Check	<u>174</u>
* * * Next Function Code * * *	
* * * Next Function Code * * *	
Read/Write Data	<u>170</u>
Number Low Bytes	<u>168</u>
Number High Bytes	<u>166</u>
Starting Low Address	<u>164</u>
Starting High Address	<u>162</u>
Sub-Index	<u>160</u>
Index Low	<u>158</u>
Index High	<u>156</u>
Node ID	<u>154</u>
Extend Bit	<u>152</u>
Reference Type	<u>150</u>
Function Code	
Reserved Byte	
Function Code "41"	
Slave ID	<u>172</u>

Fig. 2C



240 ↘

Error Check	<u>274</u>
* * * Next Function Code * * *	
* * * * * * *	
* * * Next Function Code * * *	
Slave ID	<u>272</u>

Fig. 2D

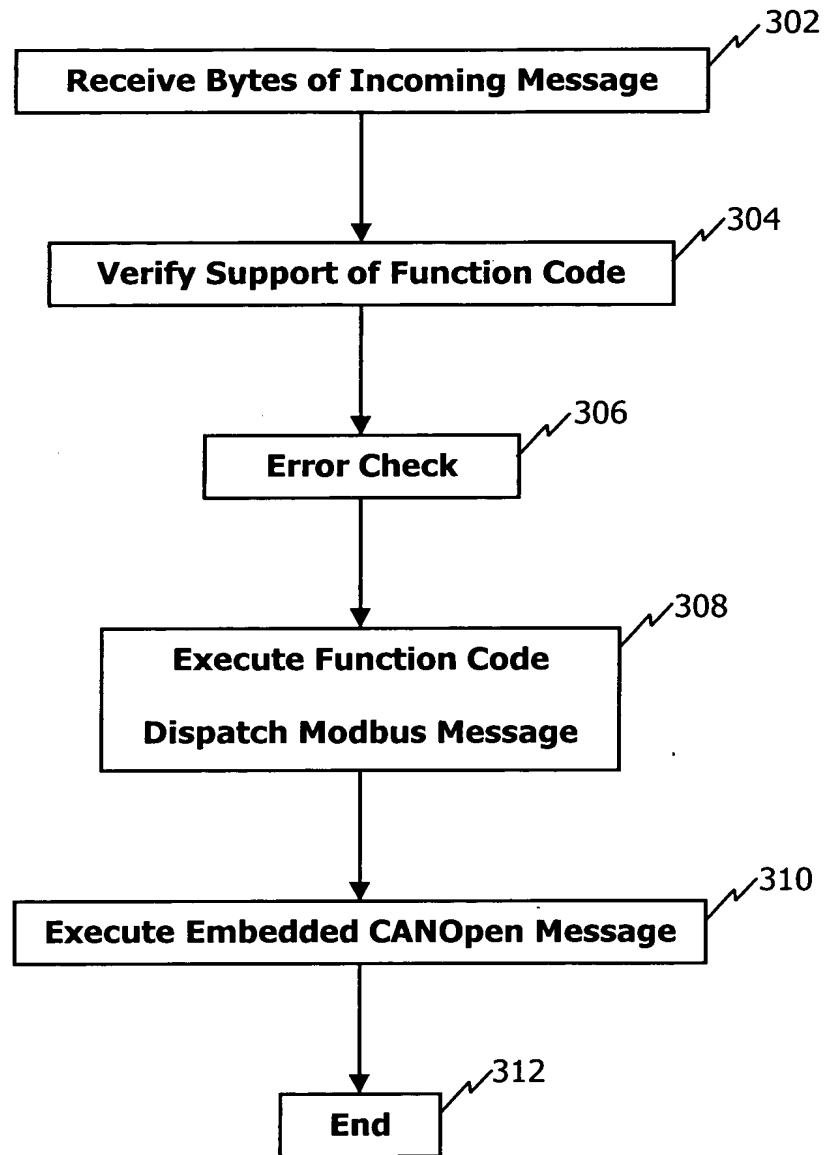
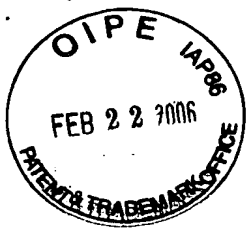


Fig. 3



Modbus function code	Sub- function or sub-index	Command
3		Read 4x registers
4		Read 3x registers
16		Write 4x registers
22		Mask write
23		Combination 4x read/write
43		Read Object Dictionary entries
43		Write Object Dictionary entries
43	1	COMS-Reset_req
43	2	Start_BootUpAuto_req
43	3	Command not used
43	4	Run_Network_req
43	5	Stop_Network_req
43	6	Store_Config_req
43	7	Store_Config_Sim_req
43	8	Restore_Config_req
43	9	Request mastery over PI output data for AI-config-tool
43	10	Release mastery over PI output data for AI-config-tool
43	11	Request mastery over application parameter area for AI-config-tool
43	12	Release mastery over application parameter area for AI-config-tool
43	13	Save password for access via config port
43	14	Set FBC into protected mode
43	15	Set FBC into edit mode (= leave protected mode)
125		Flash programming commands
125	1	Read hardware identification
125	2	Not supported
125	3	Not supported
125	4	Confirm mode
125	5	Enter kernel mode
125	6	Exit kernel mode
125	7	Fill flash memory
125	8	Program flash memory
125	9	Read flash memory
126		Programming commands
126	1	Stop
126	2	Start

Fig. 4